



# UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
[www.uspto.gov](http://www.uspto.gov)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/937,796	01/28/2002	Mitsuhiro Koden	70904-56520	7040
21874	7590	09/16/2004	EXAMINER	
EDWARDS & ANGELL, LLP P.O. BOX 55874 BOSTON, MA 02205				LAVARIAS, ARNEL C
ART UNIT		PAPER NUMBER		
		2872		

DATE MAILED: 09/16/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Advisory Action</b>	<b>Application No.</b>	<b>Applicant(s)</b>
	09/937,796	KODEN ET AL.
	<b>Examiner</b>	<b>Art Unit</b>
	Arnel C. Lavarias	2872

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 23 August 2004 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

**PERIOD FOR REPLY [check either a) or b)]**

- a)  The period for reply expires \_\_\_\_\_ months from the mailing date of the final rejection.
- b)  The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.  
ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1.  A Notice of Appeal was filed on \_\_\_\_\_. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.
2.  The proposed amendment(s) will not be entered because:
  - (a)  they raise new issues that would require further consideration and/or search (see NOTE below);
  - (b)  they raise the issue of new matter (see Note below);
  - (c)  they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
  - (d)  they present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: \_\_\_\_\_.

3.  Applicant's reply has overcome the following rejection(s): \_\_\_\_\_.
4.  Newly proposed or amended claim(s) \_\_\_\_\_ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
5.  The a) affidavit, b) exhibit, or c) request for reconsideration has been considered but does NOT place the application in condition for allowance because: See Continuation Sheet.
6.  The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.
7.  For purposes of Appeal, the proposed amendment(s) a) will not be entered or b) will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: \_\_\_\_\_.

Claim(s) objected to: \_\_\_\_\_.

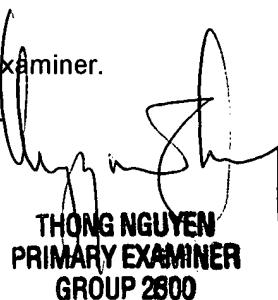
Claim(s) rejected: 1-30.

Claim(s) withdrawn from consideration: \_\_\_\_\_.

8.  The drawing correction filed on \_\_\_\_\_ is a) approved or b) disapproved by the Examiner.

9.  Note the attached Information Disclosure Statement(s) (PTO-1449) Paper No(s). \_\_\_\_\_.

10.  Other: \_\_\_\_\_.



THONG NGUYEN  
PRIMARY EXAMINER  
GROUP 2800

Continuation of 5. does NOT place the application in condition for allowance because: Applicants' remarks have been considered, but are not found persuasive. Regarding the limitation that '... the at least one light output layer shines when a specified time has elapsed after a complete set of data signals for each scan line is transmitted to the first electrodes and extinguishes before a succeeding complete set of data signals for each scan line is transmitted.', it is noted that Figure 8 of Tajima et al. (See also col. 3, line 66-col. 5, line 53 of Tajima et al.) specifically discloses that during each subframe SF1, SF2, ..., SF6, of a single 16.7 ms frame, the light output layer shines (i.e. during display period S3) when a specified time has elapsed after a complete set of data signals for each scan line (i.e. the addressing signals provided to address electrode during time period S2, wherein during each subframe, a complete set of addressing signals is provided to the Y-Sustain electrodes) is transmitted to the first electrodes (i.e. the Y-Sustain electrodes) and extinguishes (i.e. during the 'OFF' period S1) before a succeeding complete set of data signals for each scan line is transmitted. Further, with regard to the limitation that '... the light output layer is adjusted in terms of luminance to a maximum luminance of the data signals for each scan line.', it is noted that the addressing signals provided to the address electrode is given a voltage of either 0 volts or VA volts (See Figure 8), i.e. either the light output layer is off or on. The luminance of the light output layer is adjusted via a combination of the application of an address signal to the address electrode and the number of sustain discharge pulses applied during display period S3 (See also Figure 10; col. 5, lines 3-31), wherein a maximum luminance is achieved by providing an address signal to the address electrode and maximizing the number of sustain discharge pulses during the display period S3.